

## PART B - FEE(S) TRANSMITTAL

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Joseph Jong

(Depositor's name)

(Signature)

November 6, 2006

(Date)

46798 7590 08/08/2006  
 PATTERSON & SHERIDAN, LLP  
 Gero McClellan / Infineon Technologies  
 3040 POST OAK BLVD.,  
 SUITE 1500  
 HOUSTON, TX 77056  
 11/07/2006 RHEBRAH1 00000017 200782 10777992

01 FC:1501

1400.00 DA

02 FC:1502

300.00 DA

APPLICATION NO.	DAILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/777,992	02/12/2004	Georg Braun	INF 2166-US	3317

TITLE OF INVENTION: METHOD AND CIRCUIT FOR ALLOCATING MEMORY ARRANGEMENT ADDRESSES

APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$0	\$1700	11/08/2006
EXAMINER	ART UNIT	CLASS-SUBCLASS				
SCHLIE, PAUL W	2186	711-170000				

Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

☐ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.

☐ "Fee Address" indication (or "Fee Address" Indication form PTO/SB/47; Rev 03-02 or more recent) attached. Use of a Customer Number is required.

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1 Patterson & Sheridan, L.L.P.

2

3

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(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

INFINEON TECHNOLOGIES AG

MUNICH, GERMANY

case check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

1. The following fee(s) are submitted:

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Change in Entity Status (from status indicated above)

☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27.

☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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Authorized Signature

Date

November 6, 2006

Typed or printed name

Joseph M. Jong

Registration No.

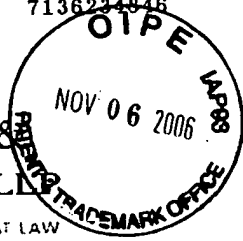
42,698

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## FACSIMILE COVER SHEET

**DATE:** November 6, 2006

**FILE NO:** INFN/WB0059 (INF 2166-US)

**TO:** **Mail Stop ISSUE FEE**  
Commissioner for Patents, USPTO

**FAX NO:** 1-571-273-2885

**FROM:** Gero G. McClellan / Joseph M. Jong

**PAGE(S) with cover:** 3

**ORIGINAL TO FOLLOW?** ☐ YES ☒ NO

**RE:**

**TITLE:** METHOD AND CIRCUIT FOR ALLOCATING MEMORY  
ARRANGEMENT ADDRESSES

**U.S. SERIAL NO.:** 10/777,992

**FILING DATE:** February 12, 2004

**INVENTOR:** Georg Braun et al.

**EXAMINER:** Paul W. Schlie

**ART UNIT:** 2186

**CONF. NO.:** 3317

**Attached are:**

1. Part B - Fee(s) Transmittal for Application No. 10/777,992
2. Copy for charge to Deposit Account No. 20-0782 / INFN/WB0059 / GGM

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